

FORM PTO-1449 U.S. DEPT. OF COMMERCE
(Rev. 2-32) PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. 2115.007

SERIAL NO. 08/512,505

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

APPLICANT: Lizy Kurian John

(Use several sheets if necessary)

FILING DATE: 08/08/95

GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS
	4 6 2 2 6 4 8	11/11/86	S. Whitaker	364	715
	4 7 5 8 7 4 5	07/19/88	Elgamal et al.	307	465
	4 8 7 3 4 5 9	10/10/89	El Gamal et al.	307	465
	5 0 2 1 6 8 9	06/04/91	Pickett et. al.	307	465
	5 1 4 4 5 8 2	09/01/92	R. Steele	365	189
	5 2 3 7 2 1 9	08/17/93	R. Cliff	307	465
	5 2 5 5 2 2 1	10/19/93	D. Hill	365	189.08
	5 3 0 7 3 2 0	04/26/94	S. Farrer	365	230.01
	5 3 1 7 2 1 2	05/31/84	S. Wahlstrom	307	465
	5 3 3 1 2 2 6	07/19/94	F. Goetting	307	465

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

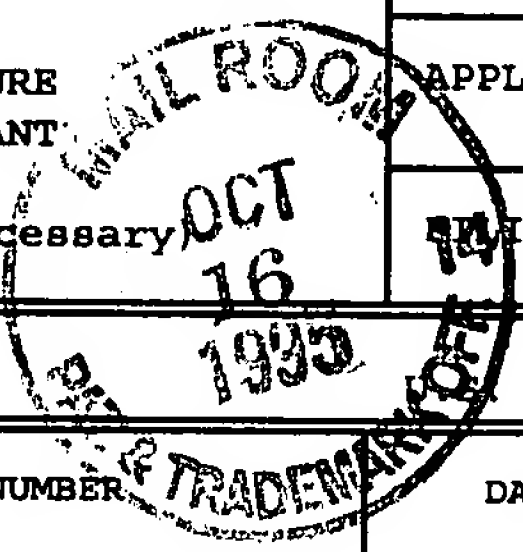
	IEEE Spectrum - October 1992
	"Memory Catches Up", pages 34-35
	IEEE Spectrum - October 1992
	"Fast computer memories", pages 36-39
	IEEE Spectrum = October 1992
	"Fast DRAMs for Sharper TV", pages 40-42
Examiner <i>four</i>	Date Considered 7-97

Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])

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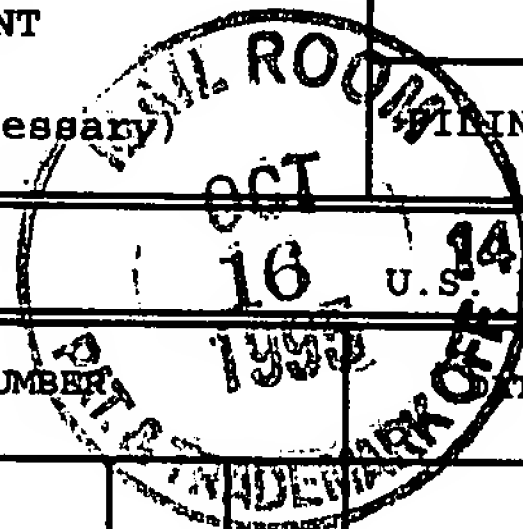
FOREIGN PATENT DOCUMENTS													
		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)	
<div style="font-size: 2em;">}</div>	IEEE Spectrum - October 1992
	"A RAM link for high speed", pages 52-53
<div style="font-size: 2em;">}</div>	IEEE Spectrum - October 1992
	"Fast interfaces for DRAMs", pages 54-57
<div style="font-size: 2em;">}</div>	IEEE Spectrum - February 1994
	"Memory in the fast lane", pages 38-41
Examiner <i>Jaw</i>	Date Considered <i>7-97</i>

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<p>APPLICANT: Lizy Kurian John</p>		
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PATENT DOCUMENTS											
EXAMINER INITIAL		DOCUMENT NUMBER						DATE	NAME	CLASS	SUBCLASS

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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)	
	Computer Design - March, 1993
	Wen Jai Hsieh on: "Reconfigurable hardware", pages 27-28 and 30
	Computer Design - March, 1993 "Cache architectures under pressure to match CPU performance", pages 91-97
	Motorola, Inc. 1993 Product Review "64K x 18 Bit BurstRAM Synchronous Fast Static RAM With Burst Counter and Self-Timed Write"
Examiner	Date Considered 7-97

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